

The diagram illustrates a neural network architecture for a control system. It consists of several interconnected blocks and signal paths:

- Block 11:** A rectangular block that receives an input signal and outputs a signal P .
- Block 16:** A rectangular block that receives the signal P and outputs a signal $\lambda(P)$.
- Block 13:** A rectangular block labeled E that receives an input K and two other inputs, W_1 and W_2 . It outputs a signal $W(K)$.
- Block 15:** A rectangular block that receives the signal $W(K)$ and outputs a signal $W(K, P)$.
- Block 17:** A rectangular block that receives the signal $W(K, P)$ and the signal $\lambda(P)$ from block 16. It outputs a signal W .
- Block 18:** A circular block with an 'x' inside, representing a multiplication operation. It receives the signal W from block 17 and a signal d from the left. It outputs a signal \bar{W} .
- Block 12:** A circular block with a '+' inside, representing an addition operation. It receives the signal \bar{W} from block 18 and the signal P from block 11. It outputs a signal Q .

The overall system flow is as follows: The input K and weights W_1, W_2 are processed by block E (13) to produce $W(K)$. This signal is then processed by block 15 to produce $W(K, P)$. The signal P from block 11 is also processed by block 16 to produce $\lambda(P)$. Both $W(K, P)$ and $\lambda(P)$ are inputs to block 17, which produces W . This signal W is then multiplied by d in block 18 to produce \bar{W} . Finally, \bar{W} and P are added in block 12 to produce the output Q .

A diagram illustrating a 2D lattice structure. The lattice is composed of a grid of N_1 rows and N_2 columns of square cells. Each cell contains a stylized 'W' shape. The horizontal distance between the centers of adjacent 'W' shapes in a row is labeled M . The vertical distance between the centers of adjacent 'W' shapes in a column is also labeled M . A label $W(K)$ with a pointer indicates one of the 'W' shapes in the grid.

FIG. 7

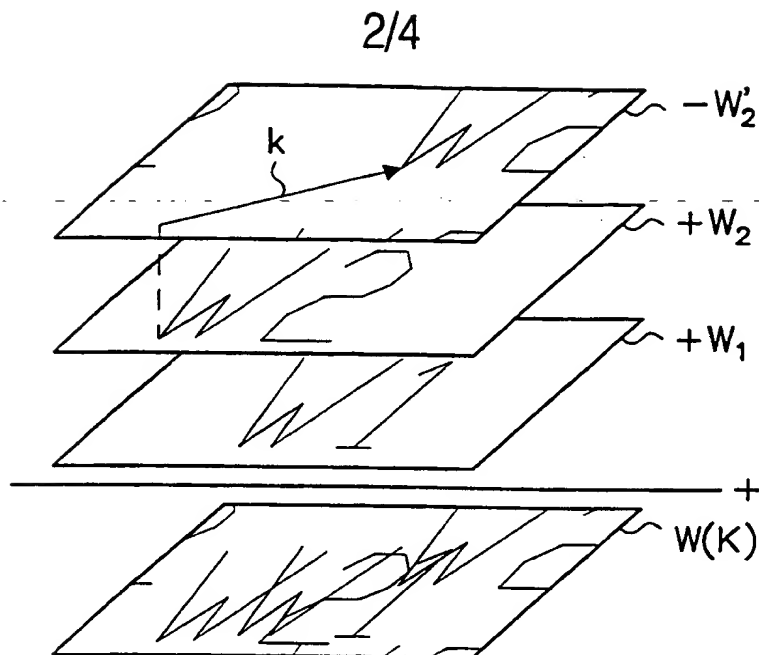


FIG. 3

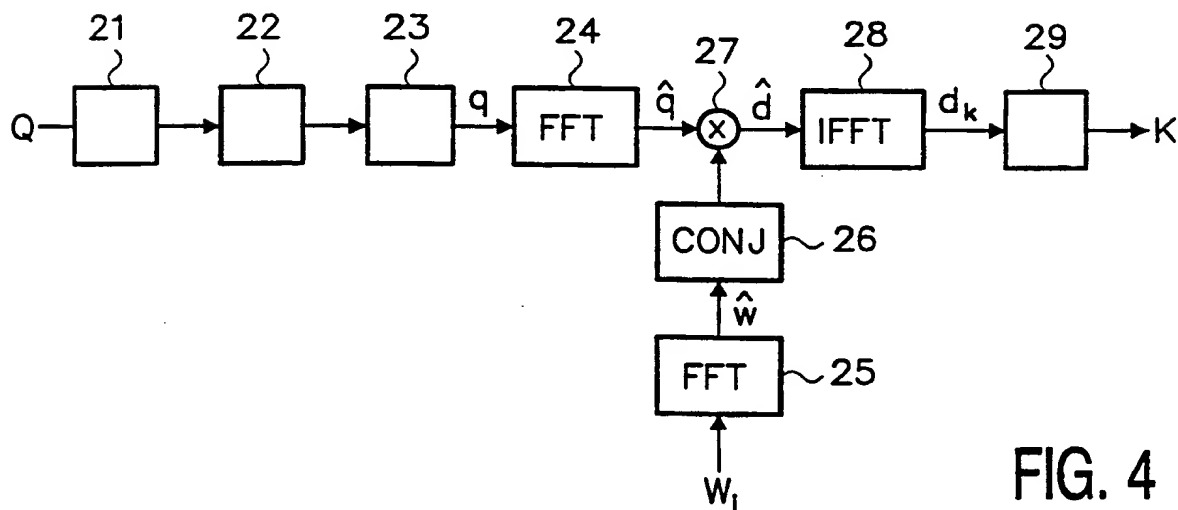


FIG. 4

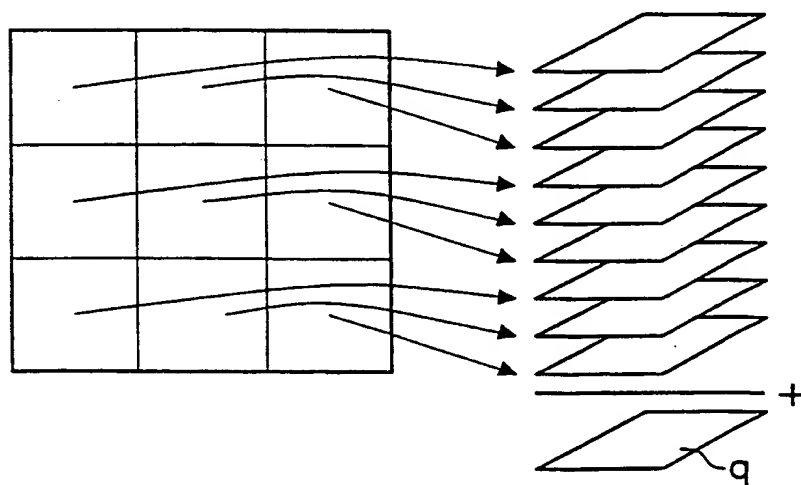


FIG. 5

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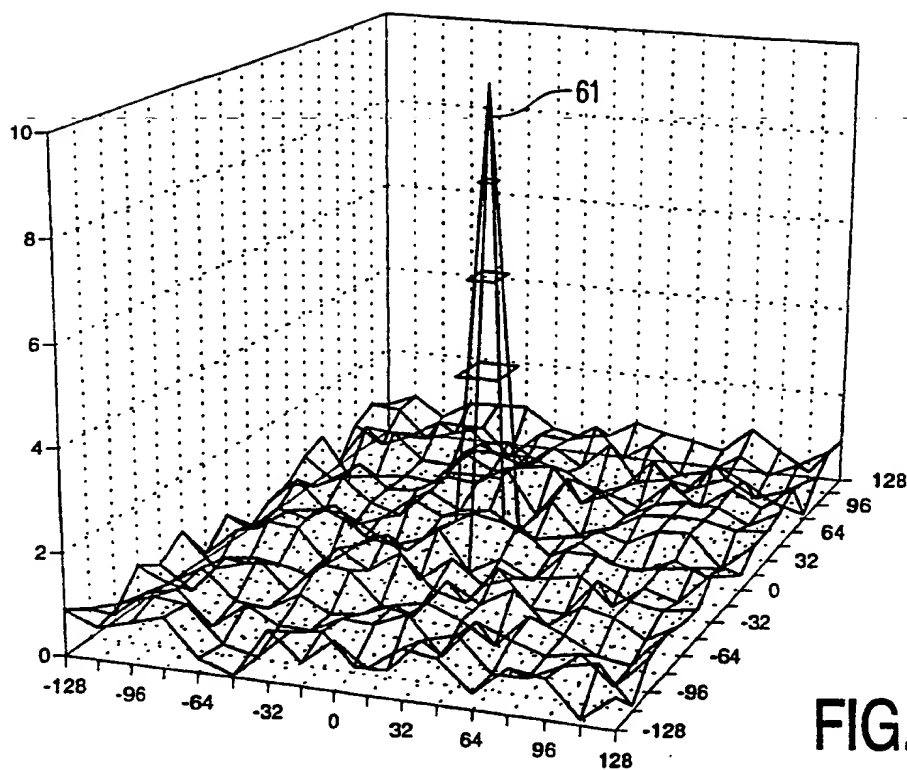


FIG. 6A

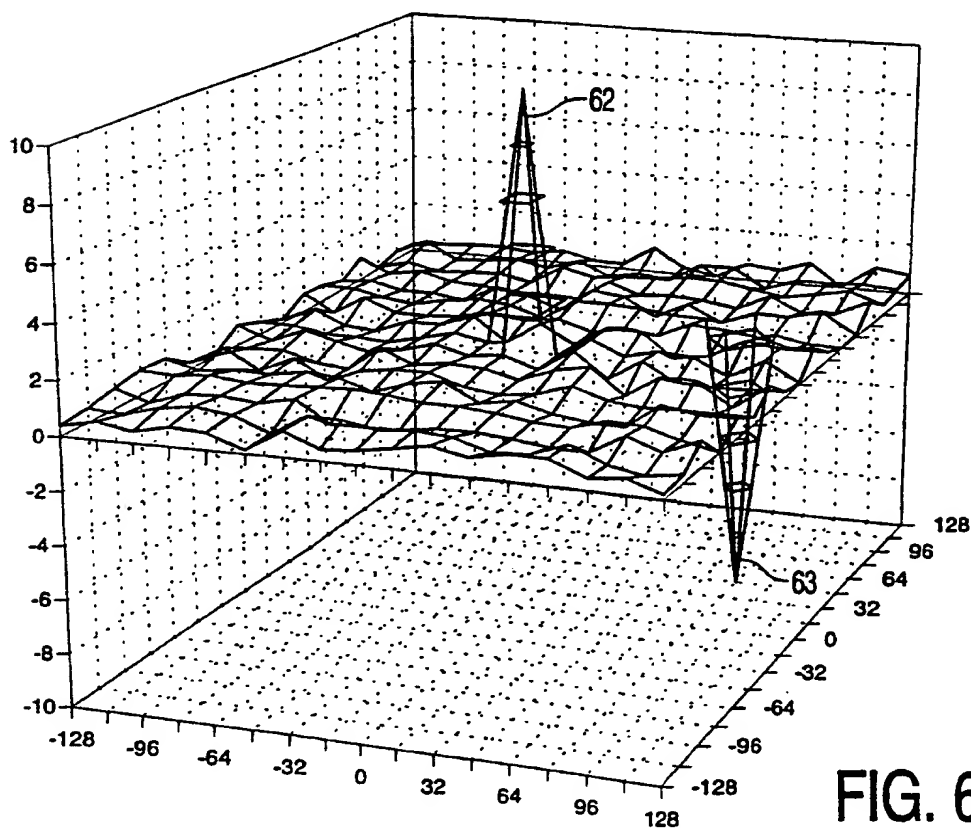


FIG. 6B

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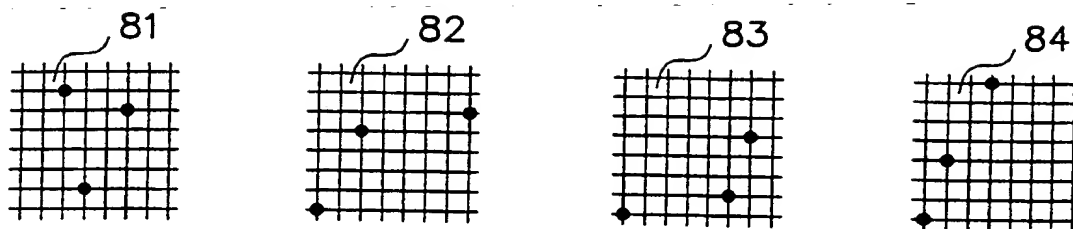


FIG. 8

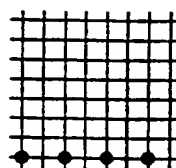


FIG. 9